

WHAT IS CLAIMED IS:

1. An image display device having an active matrix substrate provided with a pixel region having a large number of pixels arranged in a matrix configuration, and a drive circuit region disposed outside  
5 of said pixel region for supplying drive signals to said large number of pixels via interconnection lines,

wherein

said drive circuit region comprises a plurality of stages of circuit sections successively processing an externally supplied  
10 display signal to produce a drive signal to be supplied to said pixel region, each of said plurality of stages of circuit sections having a different function,

at least one of said plurality of stages of circuit sections is provided with active elements fabricated in discontinuous  
15 converted regions formed of roughly-band-shaped-crystal silicon films having grain boundaries continuous in generally one direction, and

said active elements have a direction of movement of carriers therein in a direction of said grain boundaries.

20

2. An image display device according to claim 1, wherein said circuit sections of each of said plurality of stages are arranged along one side of said active matrix substrate at specified intervals at a periphery thereof.

25

3. An image display device according to claim 1, wherein circuit sections having said active elements formed therein are in a final output stage of said plurality of stages, and

5 said interconnection lines coupling said final output stage to said plurality of pixels are arranged at wider intervals on a pixel-region side thereof than on a final-output-stage side thereof.

4. An image display device according to claim 1, wherein said circuit sections having said active elements formed therein are  
10 arranged in two or more parallel rows along one side of said active matrix substrate at specified intervals at a periphery thereof.

5. An image display device according to claim 1, wherein said active elements are arranged along two opposed sides of said active  
15 matrix substrate at specified intervals at peripheries thereof.

6. An image display device according to claim 1, wherein areas of said circuit sections having said active elements formed therein vary with a scale thereof.

20

7. An image display device according to claim 1, wherein said circuit sections having said active elements formed therein are arranged in two or more parallel rows along one side of said active matrix substrate, and said circuit sections in one of said two or more  
25 rows are offset in longitudinal directions thereof from said circuit

sections in an adjacent one of said two or more rows.

8. An image display device according to claim 1, wherein said active elements are arranged in two or more parallel row along one side of said active matrix substrate, and said active elements in one of said two or more parallel rows are offset in longitudinal directions thereof from said active elements in an adjacent one of said two or more parallel rows.

9. An image display device according to claim 1, wherein said active elements are thin film transistor.

10. An image display device according to claim 1, further comprising a color filter substrate and a liquid crystal layer, wherein said liquid crystal layer is sandwiched between said active matrix substrate and said color filter substrate superposed on said active matrix substrate with a specified spacing therebetween.

11. An image display device according to claim 1, wherein each of said pixels further comprises an organic EL layer.